AMENDMENTS IN THE SPECIFICATION

Please replace the paragraph beginning on page 1, line 6 with the following paragraph:
The present invention is related to the subject matter of the following commonly
assigned, co-pending United States Patent Applications filed concurrently herewith:
Serial No 09/588,608 (Docket No. AT9-99-501) entitled "Multiprocessor
Speculation Mechanism Via a Barrier Speculation Flag";
Serial No 09/588,607 (Docket No. AT9-99-502) entitled "System and Method
for Asynchronously Overlapping Storage Barrier Operations With Old and New Storage
Operations";
Coming NI
Serial No <u>09/588,606</u> (Docket No. AT9-99-503) entitled "Multiprocessor Speculation Mechanism With Imprecise Recycling of Storage Operations";
speculation Mechanism with imprecise Recycling of Storage Operations,
Serial No. — 09/588,605 (Docket No. AT9-99-504) entitled "Multiprocessor
Speculation Mechanism for efficiently Managing Multiple Barrier Operations";
Serial No 09/588,509 (Docket No. AT9-99-506) entitled "Mechanism for
Folding Storage Barrier Operations in a Multiprocessor System";
Serial No 09/588,483 (Docket No. AT9-99-507) entitled "Multi-level
Multiprocessor Speculation Mechanism"; and
Serial No <u>09/588,507</u> (Docket No. AT9-99-508) entitled "System and Method
for Providing Multiprocessor Speculation Within a Speculative Branch Path".
The content of the above-referenced applications is incorporated herein by reference.

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